Jul 14 2005 7:28AM

Hr LASERJET 3330



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants:

John LACOMBE et al.

3871

09/932,541

Serial No.:

2162 Group Art Unit:

Filed:

08/17/2001

Examiner

Confirmation No.:

St. John Courtenay

For:

Hardware Implementation § Of An Application-Level

Docket No.:

200301694-1

Watchdog Timer

DECLARATION UNDER RULE 131(A)

Mail Stop Amendment Commissioner for Patents PO Box 1450 Alexandria, VA 22313-1450 Date: July 12, 2005

SIr:

John LACOMBE and Theodore F. EMERSON, applicants in the aboveidentified patent application, declare as follows:

- 1. That prior to March 31, 2001, Messrs. Lacombe and Emerson were employed as engineers for Compaq Computer Corporation;
- 2. That prior to March 31, 2001, Messrs. Lacombe and Emerson conceived of a hardware implementation of an application-level watchdog timer,
- 3. That said watchdog timer included a computer system, comprising at least one processor, a system memory coupled to said processor, at least one input/output device coupled to said processor, and a watchdog timer device, wherein the computer system executes: an operating system with at least two protection layers, one or more computer applications, and an application watchdog driver that monitors user designated computer applications for periodic messages, wherein if the watchdog driver receives a periodic message from all user-designated computer applications in a predetermined period of time, the watchdog driver delivers a command to clear the watchdog

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CONLEY, ROSE

COPY

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timer device, and further comprising a message passing interface that transmits signals between the two protection layers, wherein the watchdog driver executes in one protection layer and the application executes in another protection layer and wherein the periodic message is transmitted from the application to the application watchdog driver through the message passing interface;

- 4. That Massrs. Lacombe and Emerson received an initial draft of a patent application for said watchdog timer sometime prior to March 31, 2001, as evidenced in Exhibits A and B attached hereto; and
- 5. That after Messrs. Lacombe and Emerson received this initial draft of asid watchdog timer, subsequent revisions were made to the patent application for said watchdog timer on April 9, 2001, May 22, 2001, June 14, 2001, and July 30, 2001—prior to its eventual filing on August 17, 2001 as evidenced in Exhibit B attached hereto.

The declarants further state that the above statements were made with the knowledge that willful false statements and the like are punishable by fine and/or imprisonment, or both, under Section 1001 of Title 18 of the United States Code, and that any such willful false statement may jeopardize the validity of this application or any patent resulting therefrom.

7/12/05

John LACOMBE

DATE

Theodore F. EMERSON

15267 2,02/1682,60500

Page 2 of 2

HP PDNO 2003016944

Lacoal

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timer device, and further comprising a message passing interface that transmits signals between the two protection layers, wherein the watchdog driver executes in one protection layer and the application executes in another protection layer and wherein the periodic message is transmitted from the application to the application watchdog driver through the message passing interface;

- 4. That Messrs. Lacombe and Emerson received an initial draft of a patent application for said watchdog timer sometime prior to March 31, 2001, as evidenced in Exhibits A and B attached hereto; and
- 5. That after Messrs. Lacombe and Emerson received this initial draft of said watchdog timer, subsequent revisions were made to the patent application for said watchdog timer on April 9, 2001. May 22, 2001, June 14, 2001, and July 30, 2001—prior to its eventual filling on August 17, 2001 as evidenced in Exhibit B attached hereto.

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DATE

7/14/2005

DATE

John LACOMBE

THEODORE E EMERSON

183672 02/1882 80800

Page 2 of 2

HP PONO 200301684-

Andrew Aranda

From: Sent:

Subject:

To:

John Lacombe [JLacombe@banderacom.com]

Tuesday, March 27, 2001 10:19 AM

'Andrew Aranda'; John Lacombe; 'ted.emerson@compaq.com' RE: P00-2947 1ST DRAFT OF APPLICATION (1662-30500)

John Lacombe 8305 Cambria Drive Austin Tx, 78717 Citizenship: U.S.A.

The write up is very good. One minor addition - alteration to pg 5 section [0013]. After the application watchdog timer reaches it's threshold the action that occurs "may" be a system reset, or it may just cause an IRQ back to SW driver that can initiat a graceful restart of the operating system. (The actual mechanisms will vary per OS). The advantage being a controlled shutdown where file systems are flushed appropriately, etc...

Thanks, JL

----Original Message-----

From: Andrew Aranda [mailto:Aaranda@crtlaw.com]

Sent: Friday, March 23, 2001 9:51 AM

To: 'llacombe@banderacom.com'; 'ted.emerson@compaq.com' Subject: FW: P00-2947 1ST DRAFT OF APPLICATION (1662-30500)

Importance: High

Good morning Gentlemen,

Just a quick status check to verify that you received the message below. Please let me know if you've had a chance to review the attached draft of the patent application.

Thanks a bunch.

Regards,

Andrew R. Aranda Technical Advisor, Houston Office Conley, Rose & Tayon, P.C. 600 Travis, Suite 1800 Houston, TX 77002 713-227-7153 aaranda@crtlaw.com

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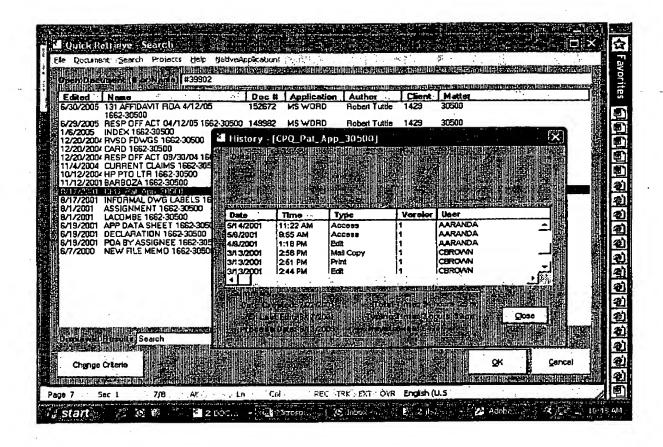


EXHIBIT "B" PAGE 1 OF 10

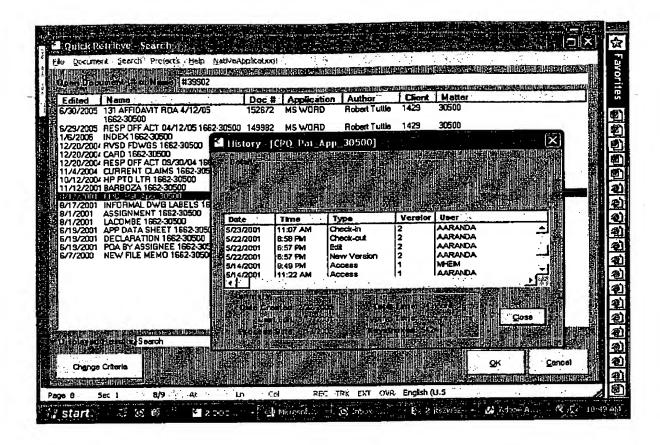


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PAGE 2 OF 10

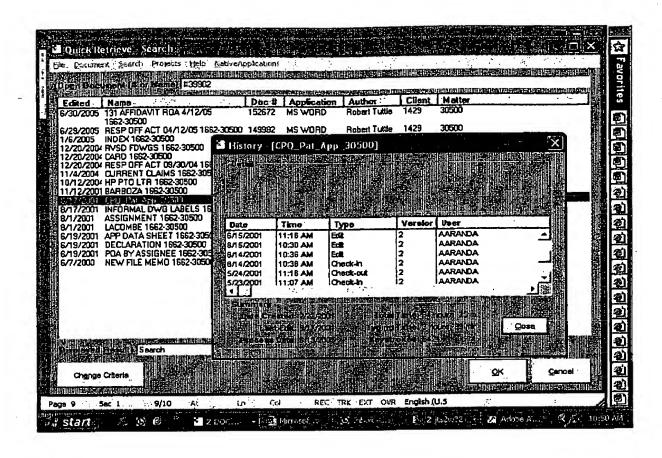


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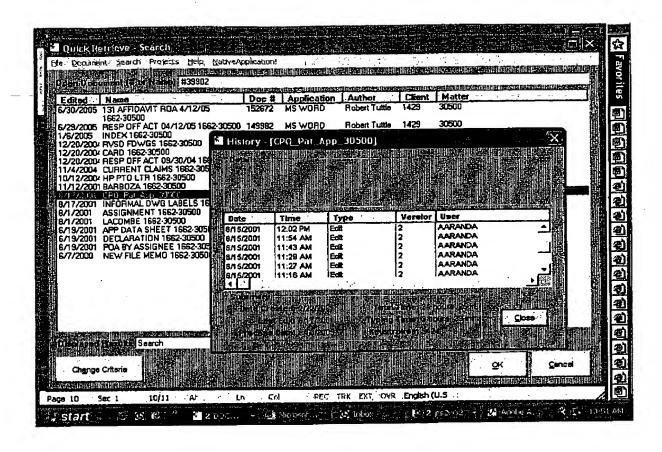


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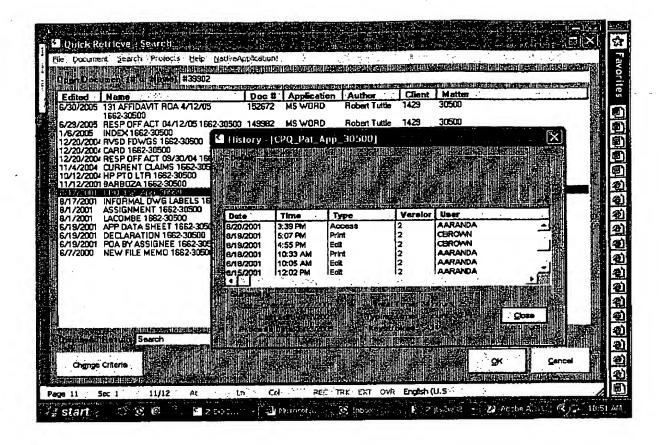


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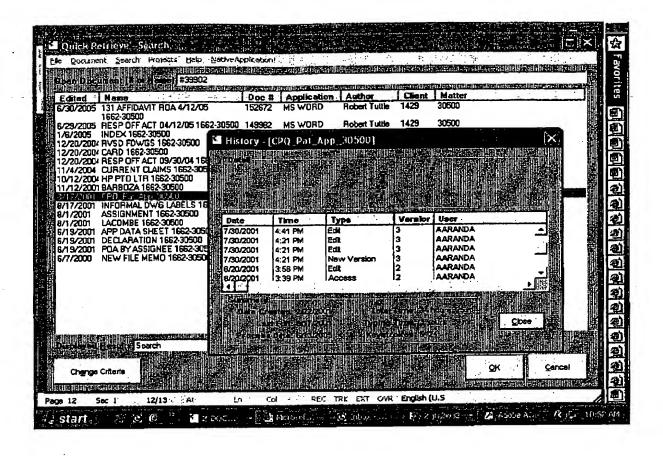


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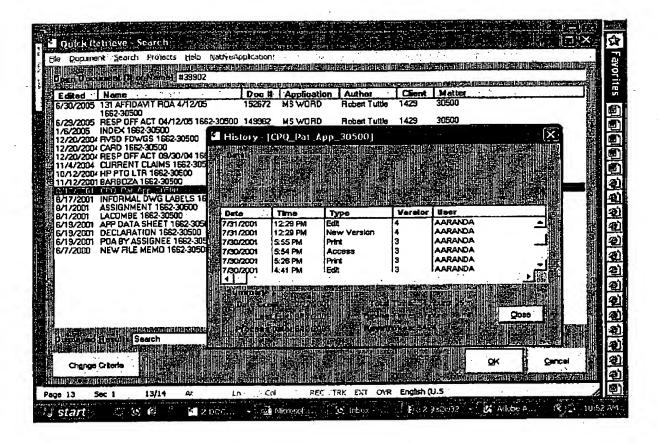


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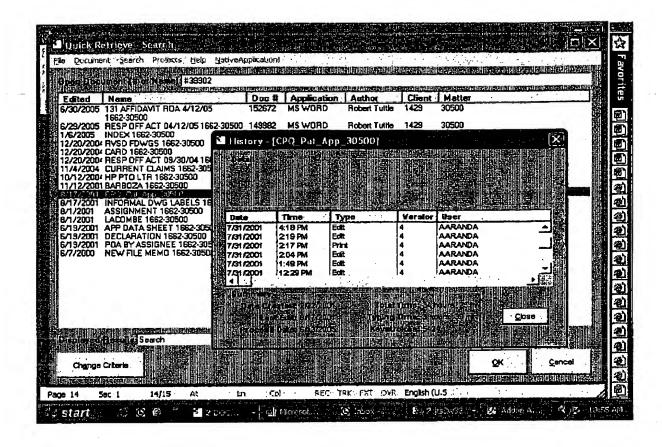


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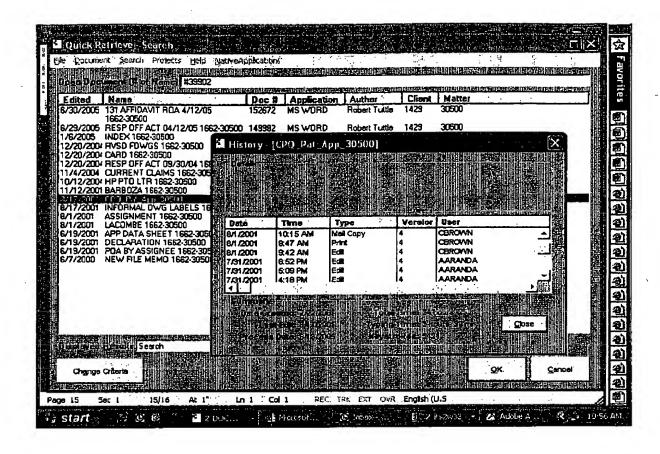


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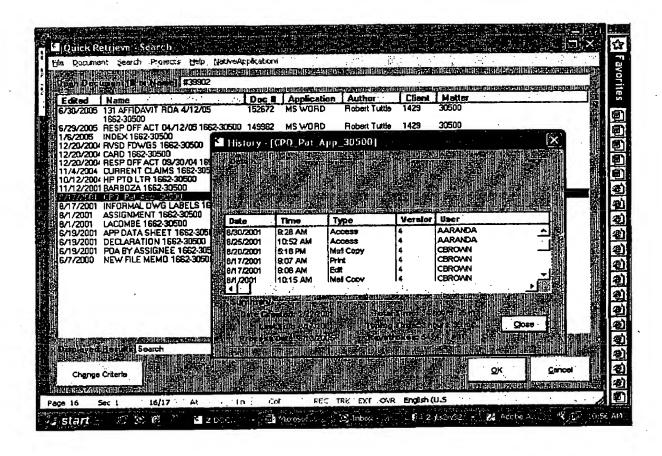


EXHIBIT "B" PAGE 10 OF 10